

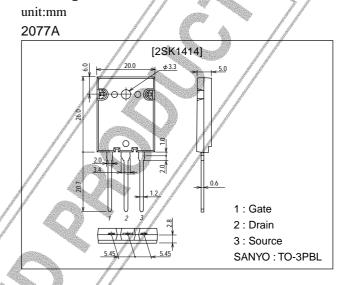
2SK1414

Ultrahigh-Speed Switching Applications

Features

- Low ON resistance, low input capacitance, Ultrahigh-speed switching.
- · High reliability (Adoption of HVP process).

Package Dimensions



Specifications

Absolute Maximum Ratings at Ta = $25^{\circ}C$

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage	VDSS	1500	V
Gate-to-Source Voltage	VGSS	±20	V
Drain Current (DC)		6	А
Drain Current (Pulse)	I _{DP} PW≤10µs, duty cycle≤1%	12	А
Allowable Power Dissipation	PD T OTO	3.5	W
	Tc=25°C	200	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

Electrical Characteristics at Ta = 25°C

Symbol	Conditions	Ratings			Unit	
Symbol	Conditions		typ	max	Onit	
V(BR)DSS	/ _D =1mA, V _{GS} =0	1500			V	
IDSS	V _{DS} =1200V, V _{GS} =0			100	μA	
IGSS	V _{GS} =±20V, V _{DS} =0			±100	nA	
VGS(off)	V _{DS} =10V, I _D =1mA	1.5		3.5	V	
yfs	V _{DS} =20V, I _D =3A	1.0	3.0		S	
R _{DS(on)}	ID=3A, VGS=10V		2.5	3.5	Ω	
	DSS GSS GSS VGS(off) /yfs	V(BR)DSS JD=1mA, VGS=0 IDSS VDS=1200V, VGS=0 IGSS VGS=±20V, VDS=0 VGS(off) VDS=10V, ID=1mA Iyfs VDS=20V, ID=3A	min V(BR)DSS ID=1mA, VGS=0 1500 IDSS VDS=1200V, VGS=0 1600 IGSS VGS=±20V, VDS=0 1.5 VGS(off) VDS=10V, ID=1mA 1.5 Vfs VDS=20V, ID=3A 1.0	Symbol Conditions min typ V(BR)DSS D=1mA, VGS=0 1500 1500 IDSS VDS=1200V, VGS=0 1 IGSS VGS=±20V, VDS=0 VGS(off) VDS=10V, ID=1mA 1.5 Ivfs VDS=20V, ID=3A 1.0 3.0	Symbol Conditions min typ max V(BR)DSS D=1mA, VGS=0 1500 1 1 1 100 100 100 100 100 1 1 <	

(Note) Be careful in handling the 2SK1414 because it has no protection diode between gate and source.

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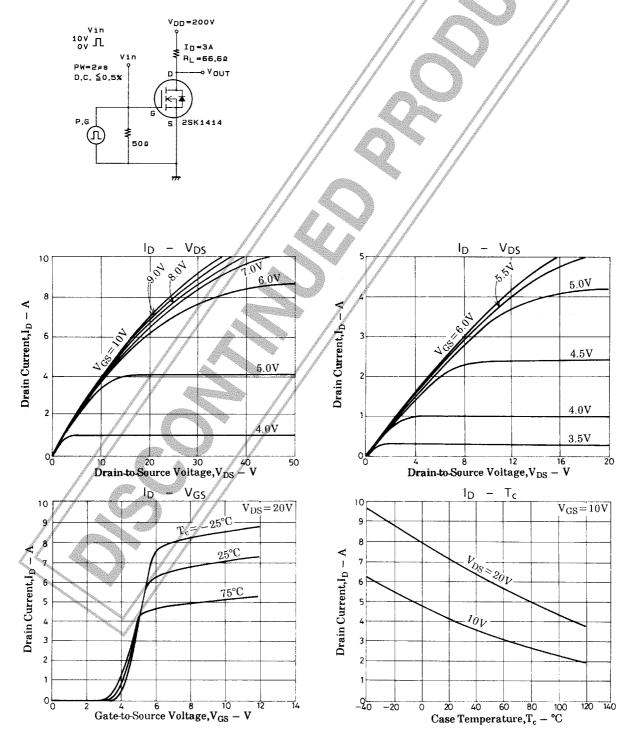
SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

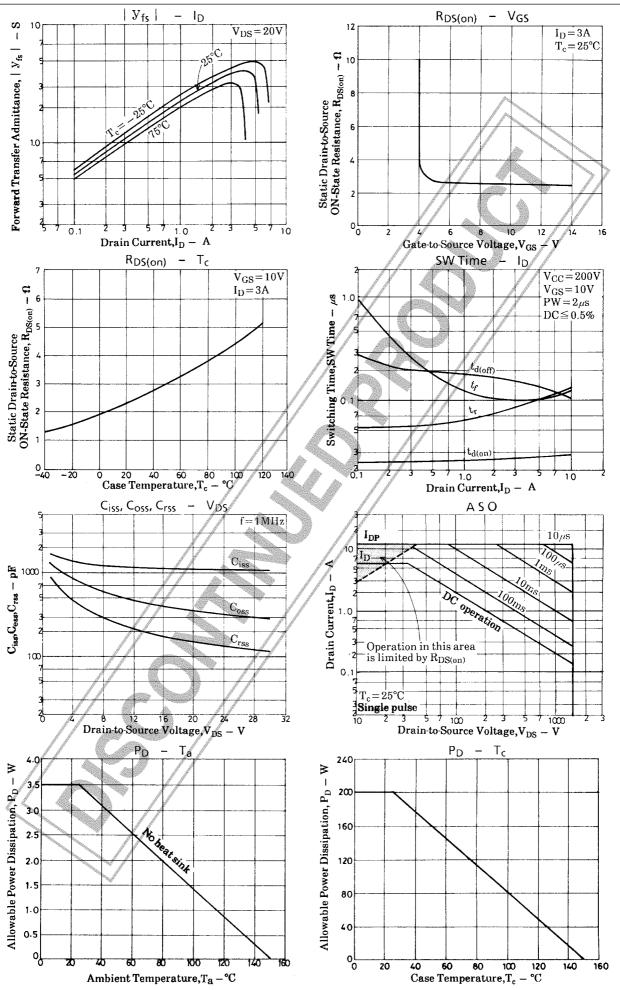
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Parameter	Symbol	Conditions	Ratings			Unit	
		Conditions		min	typ	max	
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz			1100		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz			350		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz			150		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit			25		ns
Rise Time	tr	See specified Test Circuit	1		85	and the second second	ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit			155	Contraction of the owner owner owner owner own	ns
Fall Time	t _f	See specified Test Circuit			95	and a start of the second	ns
Diode Forward Voltage	V _{SD}	I _S =6A, V _{GS} =0			1.0	1.5	V

Switching Time Test Circuit





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