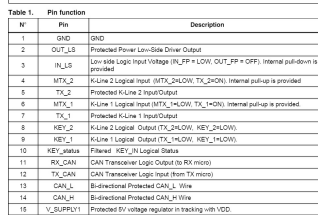


1.1 Pins function

Figure 1. Pins connection (Top view)



Pins description

L9741

Pin #	Pin function (continued)	Description
18	V _{SS}	Battery Power Supply Input. NOT PROTECTED FROM REVERSE POLARITY
19	V _{DD}	B+ Voltage regulator output
20	V _{DD} _SPV _{IN} (L)	Battery Voltage High Side Driver with current limitation (LSPIN)
	GND	Ground (LSPIN)
20	R _{ST}	Open collector V _{DD} voltage regulator RESET pin (LSPIN)
	V _{DD} _SPV _{IN} (H)	Battery Voltage High Side Driver with current limitation (LSPIN)
	GND	Ground (LSPIN)
21	CS	CS Connected to LSPIN
22	CS	Open collector V _{DD} voltage regulator RESET pin (LSPIN)
23	OSC	Rt and Cn compensation pin for each oscillator
24	GA _{IN} A	OP-AMP Input signal to set the gain value
25	GA _{IN} B	OP-AMP Input signal to set the gain value
26	GA _{IN} B	OP-AMP Input signal to set the gain value
27	N/C	Not connected
28	OUT _{OP}	Output Buffer for Op-Amp Output Voltage
29	INM _{OP}	Negative Op-Amp Input Voltage
30	INP _{OP}	Positive Op-Amp Input Voltage
31	OUT _{OP}	Output Buffer for Op-Amp Output Voltage. $OUT_{OP} \approx V_{DD} + H_{SD} \cdot I_{N_{OP}} \approx V_{DD} + I_{N_{OP}} \cdot R_{SD}$
32	SMOT	VFS Mode Input pin
33	FLM	Low Frequency Mode Input pin. This must be protected with an External 10k Ω (+/-5%) Resistor
34	IN _F	Low Frequency Mode Input pin. This must be protected with an External 10k Ω (+/-5%) Resistor
34	REXT	External Resistor Input pin. A 2.7k resistor is required at this pin and the pin will affect the high and low VFS input hysteresis.
35	KEY_IN	Push down. ON/OFF Input voltage. A HIGH level on this pin switches ON the battery voltage selected in external battery mode (SDS)

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Electrical specifications

2 Electrical specifications

2.1 Absolute maximum ratings

Table 2. Absolute maximum ratings.

Symbol	Parameter	Value	Unit
V _{BAT}	Battery Power Supply Voltage	0.3 to 4.0	V
I _{BAT}	Battery Current	1	A
V _{DD}	Logic Supply Output Voltage	0.3 to 6	V
V _{DD}	Logic Supply Output Voltage	0.3 to 1.6	V
I _{DD}	Logic Supply Output Current (Internally limited)		
	Logic Supplies (ENABLE, OSC, M1X_1, M1X_2, I, M1X_3, I, M1X_4, I, M1X_5, I, M1X_6, I, M1X_7, I, M1X_8, I, M1X_9, I, M1X_10, I, M1X_11, I, M1X_12, I, M1X_13, I, M1X_14, I, M1X_15, I, M1X_16, I, M1X_17, I, M1X_18, I, M1X_19, I, M1X_20, I, M1X_21, I, M1X_22, I, M1X_23, I, M1X_24, I, M1X_25, I, M1X_26, I, M1X_27, I, M1X_28, I, M1X_29, I, M1X_30, I, M1X_31, I, M1X_32, I, M1X_33, I, M1X_34, I, M1X_35, I, M1X_36, I, M1X_37, I, M1X_38, I, M1X_39, I, M1X_40, I, M1X_41, I, M1X_42, I, M1X_43, I, M1X_44, I, M1X_45, I, M1X_46, I, M1X_47, I, M1X_48, I, M1X_49, I, M1X_50, I, M1X_51, I, M1X_52, I, M1X_53, I, M1X_54, I, M1X_55, I, M1X_56, I, M1X_57, I, M1X_58, I, M1X_59, I, M1X_60, I, M1X_61, I, M1X_62, I, M1X_63, I, M1X_64, I, M1X_65, I, M1X_66, I, M1X_67, I, M1X_68, I, M1X_69, I, M1X_70, I, M1X_71, I, M1X_72, I, M1X_73, I, M1X_74, I, M1X_75, I, M1X_76, I, M1X_77, I, M1X_78, I, M1X_79, I, M1X_80, I, M1X_81, I, M1X_82, I, M1X_83, I, M1X_84, I, M1X_85, I, M1X_86, I, M1X_87, I, M1X_88, I, M1X_89, I, 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2.2 Thermal data

Table 3 Thermal data

Symbol	Parameter	Value	Unit
R _{thj-c}	Thermal Resistance Junction to case: PowerSO6	< 1.5	°C/W
	Thermal Resistance Junction to case: PowerSSO6	< 2.0	°C/W
R _{thj-a} ⁽¹⁾	Thermal Resistance Junction to ambient: PowerSO6	< 15	°C/W
	Thermal Resistance Junction to ambient: PowerSSO6	< 25	°C/W
T _s	Lead Temperature During Soldering (for a time = 10 s max)	260	°C

2.3 Battery voltage and temperature ranges

All function are available if:
 $5.2\text{V} < \text{VB} < \text{Vb_off}$
 $-40^{\circ}\text{C} < \text{Tj} < 150^{\circ}\text{C}$
 The device goes in switch off mode if $\text{VB} > \text{Vb_off} = 26\text{V}$ to 30V .
 The absolute maximum rating of VB is -0.3V to 40V .
 All specified parameters are valid if:
 $6.0\text{V} < \text{VB} < 24\text{V}$; $-40^{\circ}\text{C} < \text{Tj} < 25^{\circ}\text{C}$
 $10\text{V} < \text{VB} < 18\text{V}$; $25^{\circ}\text{C} < \text{Tj} < 125^{\circ}\text{C}$

2.4 Protection circuits

CAN Transceiver is protected against short circuits or overcurrents: if ICANH or ICANL exceed a current threshold I_{EC} , then CANH and CANL power transistors are switched off and transmission is disabled for ICANH/25µs typ. The two I/O lines are protected against short circuit to battery: in this event, output is switched off after a delay of 25µs typ; an input change is necessary to turn on the output transistor again.

Voltage regulator VDD and high side driver V_SUPPLY_BAT are provided with output current limitation.

Sensor supplies V_SUPPLY1 and V_SUPPLY2 are provided with output current limitation to GND and voltage protections versus short circuit to battery up to 16V.

2.5 Thermal warning and shut down

Device has a temperature sensor producing an internal thermal warning when chip temperature exceeds a threshold, with hysteresis, in the range 150°C to 200°C. In case